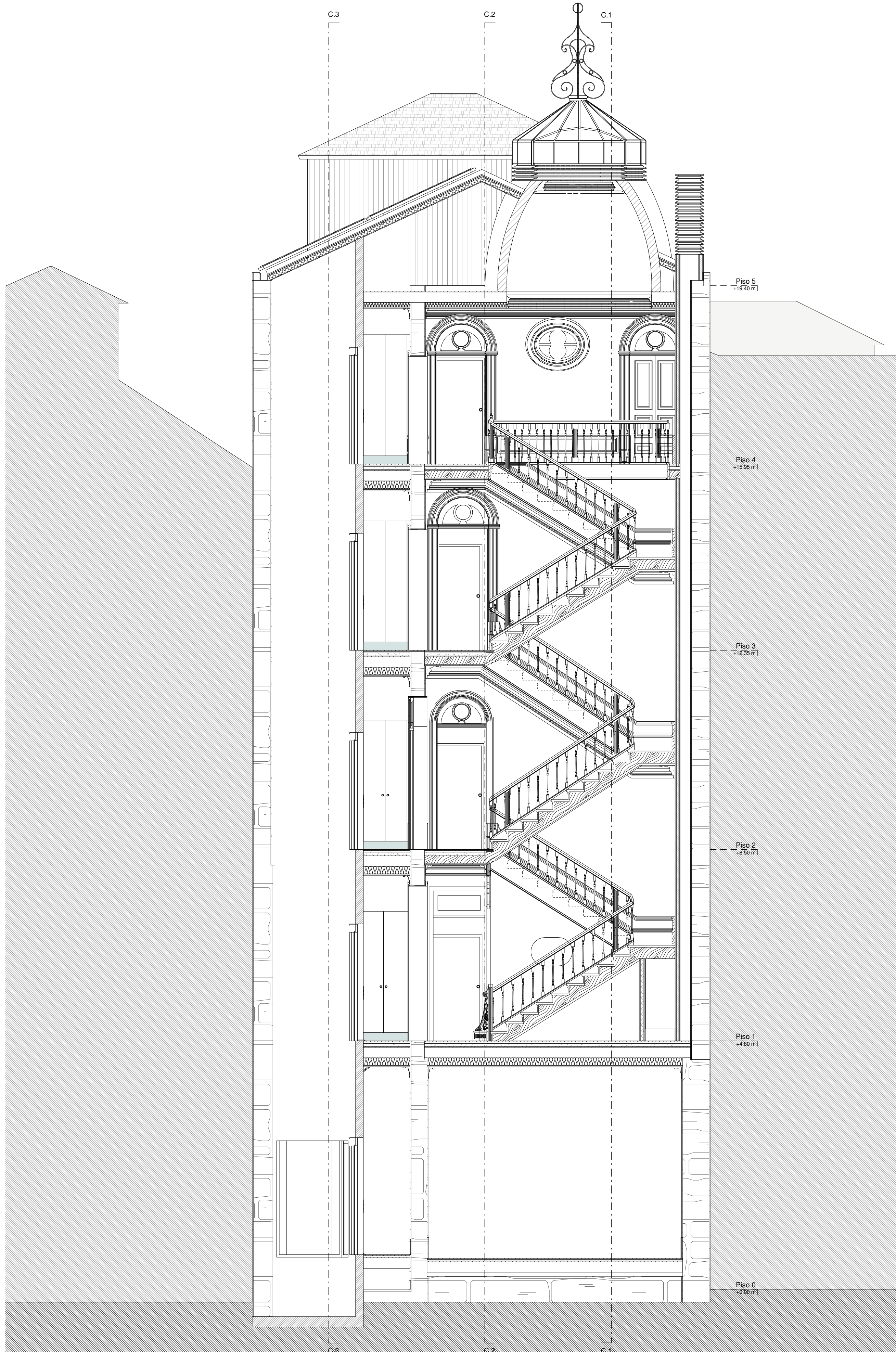




Corte C4
1 : 50



Corte C5
1 : 50